



ACADEMIC YEAR 2019-2020

The following is the list of students undergone the value added course on "FPGA BASED VLSI DESIGN" for a duration of one weeks from 28th September 2019 to 20th October 2019.

Academic year 2019-2020 (7th sem)

SI No	Name of The Student	USN	Student signature
1	RAMYA K	1SV14EC026	Ramyak
2	RASHMIN BEGAM H	1SV14EC029	Rb
3	ARUN C G	1SV15EC005	Arca
4	BILAVANA N	1SV15EC007	BN
5	GAGANA S K	1SV15EC013	gjsk
6	KAVYAS	1SV15EC018	Kavya
7	NOOR AYESHA	1SV15EC027	NA
8	RAGHURAJ G K	1SV15EC035	RGK
9	USHA Y M	1SV15EC049	USHA
10	VISHWAS S P	1SV15EC052	VSP
11	ANUSHA T P	1SV16EC402	ATP

Coordinator

PRINCIPAL
SIET, TUMAKURU.

HOD
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