VISVESVARAYA TECHNOLOGICAL UNIVERSITY "JNANA SANGAMA", BELGAVI-590018 KARNATAKA



Mini Project Report (18ECMP68)

ON

"HOME AUTOMATION UNDER WI-FI THROUGH ANDROID APPS(EX:-SMART FAN)"

Submitted in partial fulfillment of the requirement for the award of degree BACHELOR OF ENGINEERING

IN

ELECTRONICS & COMMUNICATION ENGINEERING

Submitted by:

BINDU TS MUSKAN ZAHID

Under the Guidance of:

Prof. PRADEEP KUMAR .S.S. Assistant Professor, Dept. of ECE, SIET Tumkur



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING SHRIDEVI INSTITUTE OF ENGINEERING AND TECHNOLOGY

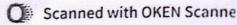
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2021-2022

Dept of E&C SiET, Tumkur-6

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2021-2022



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Certificate

This is to Certified that t	he mini project wo	rk (18ECMP68)	entitled "HOME A	UTOMATION
UNDER WI-FI THROU	JGH ANDROID A	APPS(EX:-SMA	RT FAN)" has bee	n Successfully
carried out by Bi	ndu T S and Muskan	Zahid ,	, a bonafide studen	ts of Shridevi
Institute of Engineering and Technology, Tumkur- 572106, in partial fulfillment for the				
award of Bachelor Of Engineering in Electronics & Communication Engineering of the				
Vishvesvaraya Technol				
academic year 2021-2022. It is certified that all corrections/suggestions indicated for internal				
assessments have been in	corporated in the re	port. The mini pro	oject report has been	approved as it
satisfies the academic rec	quirement with rest	pect to the mini p	roject work prescrib	ed for the said
Bachelor Of Engineering		··C	An	10

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Prof. Pradeep kumar S.S

Assistant professor Dept. of ECE., SIET

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Signature of the HOD

Signature of the principal

HOD

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Prof. Aijaz Ahamed Sharief Dr. Narendra Viswanath Principal

SIET, Tumakuru

EXTERNAL VIVA

Name of examiners:

·· Do foudeep K. G. M

Signature with date:

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ABSRACT

Images are often corrupted by impulse noise in the procedures of image acquisition and transmission. In our project an efficient VLSI implementation of Adaptive Rank Order Filter (AROF) for removal of impulse noise is proposed. The algorithm removes noise without degrading the image information. The AROF VLSI architecture implements pipelining with parallel processing in order to speed up the filtering process. The performance of proposed algorithm is compared with Decision Tree Based Denoising Method (DTBDM).

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