SHRIDEVI INSTITUTE OF ENGINEERING & TECHNOLOGY, TUMKUR-06
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

An ISO 9001:2015 Certified Institu

ACADEMIC YEAR 2019-2020

The following is the list of students undergone the value added course on "ASIC BASED VLSI DESIGN " for a duration of two weeks from 21st March 2020 to 5th April 2020.

Academic year 2019-2020 (8thsem)

Sl No	Name of The Student	USN	Student signature
1	RAMYA K	1SV14EC026	
2	RASHMIN BEGAM H	1SV14EC029	
3	ARUN C G	1SV15EC005	
4	BHAVANA N	1SV15EC007	
5	GAGANA S K	1SV15EC013	
6	KAVYA S	1SV15EC018	
7	NOOR AYESHA	1SV15EC027	
8	RAGHURAJ G K	1SV15EC035	
9	USHA Y M	1SV15EC049	
10	VISHWAS S P	1SV15EC052	
11	ANUSHA T P	1SV16EC402	

Coordinator

PRINCIPAL
SIET., TUMAKURU.

HOD

HOD
Dept. of E&C
SIET, Tumkur-6